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	A	IKEDA et al, "A Soft Error Immune 0.35 Micron PD-SOI SRAM Technology Compatible With Bulk CMOS, Proc. 1998 IEEE Int'l SOI Conf., Oct. 1998	
	В	ASIC Products Application Note, "Quality and Reliability," Rev. 3, SA14-2280-03, IBM, 1999	
	С	IBM ASIC SA-27 Databook, 1998, available on website: www.IBM.com/chips/products/asic	

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